

- 2/4/8 channel synchronous sampling
- 200/250Msps per channel, 14bit
- Input bandwidth DC-100/120MHz
- Input range: can be set as $\pm 1V$ 、 $\pm 2V$ 、 $\pm 5V$ 、 $\pm 10V$
- Input impedance: $50\Omega/1M\Omega$
- Multiple Communication Interfaces Available
- support FPGA secondary development
- Software development package supports C/C++, LabVIEW, Matlab etc



| Series | Bus | Resolution | Channel | Sampling Rate | Bandwidth | Storage depth | System support |
|-------------|-------------------------------|------------|---------------|---------------|---------------|---------------|------------------|
| LD715x-xx14 | USB3.0/RJ45 /10G optical port | 14bit | 2/4/8 channel | 200/250 Msps | DC-100/120MHz | 2GB | Windows Linux |

Brief introduction

- LD715x-xx14 is Mysoow's "Agile" series acquisition device, supporting 2/4/8 channel, 14bit, achieving 200/250Msps synchronous sampling, On board DDR3 memory granules, with 2GB storage volume, larger volume customization is also supported.
- Multiple interface buses are available, with optional support for RJ45, USB 3.0, and 10G optical ports.
- Support multiple triggering modes including hardware, triggering, software triggering and manual triggering etc. Continuous sampling mode is also supported.
- Support the secondary development for users, providing DLL dynamic link pool.
- Equipped with host computer Demo software, supporting the configuration of board and real-time display and storage of data.

Typical applications

- Optical Coherence Tomography (OCT)
- Non-destructive detection
- Wave form recorder
- Multi-channel transient recorder

Detailed parameters

| Terminal | | | | | | |
|-----------------------------|---|-----|-------|------|------|--|
| Simulated input | 2/4/8 channel, standard SMB interface, SSMA optional | | | | | |
| Triggered input | 1 way SMB | | | | | |
| Triggered output | 1 way SMB | | | | | |
| Synchronous clock input | 1 way SMB | | | | | |
| Synchronous clock output | 1 way SMB | | | | | |
| Communication bus | Optional Gigabit Ethernet port, USB 3.0 interface, and 10G optical port. | | | | | |
| IO Resources 2.0 12P box | 4 sets of IO, 3.3V/5V level software switchable, capable of external power supply. Optionally configurable with 2 sets of MLVDS or 2 sets of RS485 (can constitute RS422 interface). | | | | | |
| Acquisition system | | | | | | |
| Resolution | 14bit | | | | | |
| Bandwidth | DC-100/120MHz | | | | | |
| Input channel | 2/4/8 channel synchronous sampling | | | | | |
| Sampling rate | up to 200/250Msps | | | | | |
| Sampling mode | Continuous sampling, finite point sampling | | | | | |
| Full range input scope | $\pm 1V$ 、 $\pm 2V$ 、 $\pm 5V$ 、 $\pm 10V$ | | | | | |
| Input coupling | AC/DC coupling | | | | | |
| Input impedance | 50 Ω /1M Ω | | | | | |
| Extreme input | $\pm 40V_{max}$ | | | | | |
| Storage | | | | | | |
| Storage volume | 2GB | | | | | |
| Recording capability | Can be set up by software, the overall volume does not exceed memory space | | | | | |
| Trigger depth | Can be set up by software, the overall volume does not exceed memory space | | | | | |
| Index parameter | | | | | | |
| Input impedance | Gear | SNR | SINAD | SFDR | ENOB | Test conditions |
| 50 Ω | $\pm 2V$ | 68 | 68 | 70 | 11.1 | Input signal amplitude-1dBFS, frequency 10MHz, sampling rate 200/250Msps |
| 1M Ω | $\pm 2V$ | 65 | 65 | 68 | 10.5 | |
| Trigger Input System | | | | | | |
| Trigger source | Software trigger, threshold(channel) trigger, external(simulation and digital) trigger | | | | | |
| Channel number | 1 channel, supporting simulation and digital TTL, LVTTTL, Input impedance 1M Ω | | | | | |
| Trigger input level | Simulation input $\pm 0.2V \sim \pm 5V$, standard digital TTL, LVTTTL level, square wave/pulse wave/trapezoidal wave | | | | | |
| Trigger frequency | $\leq 2MHz$ | | | | | |
| Trigger mode | post-trigger, pre-trigger, delay-trigger, rising edge trigger, falling edge trigger, double-sided edge trigger | | | | | |

| | |
|--------------------------------|--|
| Trigger threshold adjustment | $\pm 0.2V \sim \pm 5V$ precisely adjustable |
| Trigger input width | $\geq 50ns$ |
| Trigger delay | $0 \sim 2^{31}$ sampling cycle |
| Trigger output | |
| Trigger output channel | 1 channel, share terminal with clock output |
| Trigger output level | LVTTL, output current 10mA |
| Trigger output width | $\geq 50ns$, adjustable pulse width |
| Clock system | |
| Clock source | Internal/external/external direct sampling clock source |
| Feature of internal clock | 10MHz, $\pm 2ppm$ |
| External input clock amplitude | 0.4Vpp ~ 3.3Vpp sine wave or square wave |
| External input impedance | 50 Ω |
| External input coupling mode | AC coupling |
| External input frequency range | 10MHz, 10MHz~maximum sampling rate@external direct sampling mode |
| Clock output | 10MHz@LVTTL, same source as sampling clock |
| Power requirements | |
| Power supply mode | Adapter-powered or Powered by adapter, DC005 2.0mm |
| Power requirement | Rated Voltage 12V, Supports 12~24V Input |
| Size and weight | |
| Size | length×width×height: 200×127×48mm |
| Weight | ~0.64kg/0.76kg |
| Environment parameters | |
| Working temperature | 0°C ~ +50°C |
| Relative working humidity | 10% ~ 90%RH, no condensation |
| Storage temperature | -40°C ~ +85°C |
| Relative storage humidity | 5% ~ 95%RH, no condensation |

System requirements

- 1> larger than 4GB memory, 1GB hard-disk space;
- 2> Display screen resolution larger than 1280×1024;
- 3> Operation system supports Windows, Linux and domestically produced operation system;

Software Development Package

Provide software development package to help users to quickly accomplish application development

and integration. MSDK software development package is applicable for Windows, Linux and other operation system, supporting the secondary integration and development for C/C++, Matlab, Labview, Python, C#, QT and other software, including host computer software, interface pool, DEMO routine and development description documents etc.

Ordering information

| Ordering Information | |
|----------------------|--|
| Model | Description |
| LD7152R-200M14 | 2channel 14bit, 200Msps, RJ45, bandwidth DC-100MHz |
| LD7152R-250M14 | 2channel 14bit, 250Msps, RJ45, bandwidth DC-120MHz |
| LD7154R-200M14 | 4channel 14bit, 200Msps, RJ45, bandwidth DC-100MHz |
| LD7154R-250M14 | 4channel 14bit, 250Msps, RJ45, bandwidth DC-120MHz |
| LD7158R-200M14 | 8channel 14bit, 200Msps, RJ45, bandwidth DC-100MHz |
| LD7158R-250M14 | 8channel 14bit, 250Msps, RJ45, bandwidth DC-120MHz |
| LD7152U-200M14 | 2channel 14bit, 200Msps, USB3.0, bandwidth DC-100MHz |
| LD7152U-250M14 | 2channel 14bit, 250Msps, USB3.0, bandwidth DC-120MHz |
| LD7154U-200M14 | 4channel 14bit, 200Msps, USB3.0, bandwidth DC-100MHz |
| LD7154U-250M14 | 4channel 14bit, 250Msps, USB3.0, bandwidth DC-120MHz |
| LD7158U-200M14 | 8channel 14bit, 200Msps, USB3.0, bandwidth DC-100MHz |
| LD7158U-250M14 | 8channel 14bit, 250Msps, USB3.0, bandwidth DC-120MHz |
| LD7152T-200M14 | 2channel 14bit, 200Msps, 10G optical port, bandwidth DC-100MHz |
| LD7152T-250M14 | 2channel 14bit, 250Msps, 10G optical port, bandwidth DC-120MHz |
| LD7154T-200M14 | 4channel 14bit, 200Msps, 10G optical port, bandwidth DC-100MHz |
| LD7154T-250M14 | 4channel 14bit, 250Msps, 10G optical port, bandwidth DC-120MHz |
| LD7158T-200M14 | 8channel 14bit, 200Msps, 10G optical port, bandwidth DC-100MHz |
| LD7158T-250M14 | 8channel 14bit, 250Msps, 10G optical port, bandwidth DC-120MHz |

Note: The product is not equipped with coaxial cables by default, and our company can provide customized wire services

Chengdu Mysoow Electric Co.,LTD

Tel: +86-028-87409729

Whats: 8617348137889

Wechat: 17348137889

E-Mail:marketing@mysoow.com

Address: 1st Floor, Building B, No. 3 Tianhong Road,
High-tech West District, Chengdu, Sichuan Province, China

Website: www.mysoow-global.com